

Method and Apparatus for Controlling a Single Control Port

COMPUTER PROGRAM LISTING APPENDIX

A compact disc (CD) deposited in the U.S. Patent and Trademark Office as an Appendix hereto contains computer program listings that are hereby incorporated-by-reference. The CD contains the following computer program listings, which have been prepared in the 'c' programming language and, which have been formatted as text files:

- Coolingfanc.txt
- Coolingfanh.txt
- Demuxc.txt
- Demuxh.txt
- Ir_inputc.txt
- Ir_inpuh.txt
- Ir_modulationc.txt
- Ir_modulationh.txt
- Kenwoodc.txt
- Kenwoodh.txt
- Linedriverfc.txt
- Linedriverh.txt
- Lowlatencyirc.txt
- Lowlatencyirh.txt
- Mainc.txt
- Pioneerfc.txt
- Pioneerh.txt
- Rs232c.txt
- Rs232h.txt
- Slinkc.txt
- Slinkh.txt
- Slipc.txt
- Sliph.txt
- Toolsh.txt

- Twirpirc.txt
- Twirpirh.txt
- Versionh.txt

Also included are two files: Makefile.txt and Atmel_mainapp.txt, which define how the 'c' files are built to create a software system. The "Makefile.txt" file defines the system in memory and the "Atmel_mainapp.txt" file is a linker script for mapping the system in memory. The system is preferably built using the AVR-GCC C compiler (and assembler) that is made available through the GNU project, although one of ordinary skill in the art would be able to use any suitable 'c' compiler. The system operates using the Atmel™ Mega16 microprocessor, although one of ordinary skill in the art would be able to use other processors as well.

Also included on the CD are the following VHDL files:

- Bidirbufw1.txt
- Fmw000027_xx.txt
- Irmodulator.txt
- Slinkwirxr.txt
- Tribuffw1.txt

The VHDL files may be used to program an Altera EPM7032A CPLD (Complex Programmable Logic Device).

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